

NCE Automotive N-Channel Super Trench II Power MOSFET

Description

The NCEAP25ND10AG uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

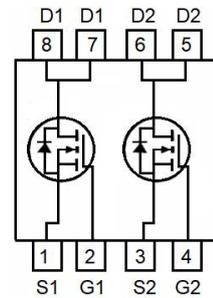
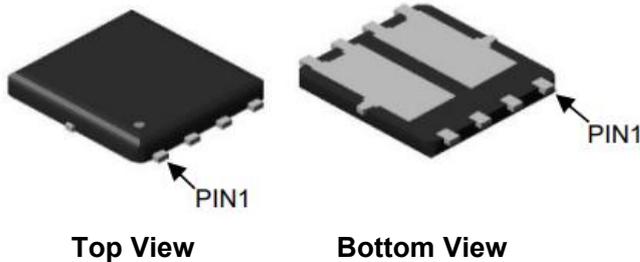
Application

- Automotive application
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

- $V_{DS} = 100V, I_D = 31A$
- $R_{DS(ON)} = 21.5m\Omega$ (typical) @ $V_{GS} = 10V$
- $R_{DS(ON)} = 30m\Omega$ (typical) @ $V_{GS} = 4.5V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- 100% UIS tested
- 100% ΔV_{ds} tested
- Pb-free lead plating
- **AEC-Q101 qualified**

PDFN 5X6-8L



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AP25ND10AG	NCEAP25ND10AG	PDFN5X6-8L	Ø330mm	12mm	5000units

Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	31	A
Drain Current-Continuous($T_c = 100^\circ C$)	$I_D(100^\circ C)$	22	A
Pulsed Drain Current	I_{DM}	128	A
Maximum Power Dissipation	P_D	54	W
Derating factor		0.36	W/ $^\circ C$
Single pulse avalanche energy (Note 1)	E_{AS}	64	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	2.78	$^{\circ}C/W$
Thermal Resistance,Junction-to-Ambient (Note 4)	$R_{\theta JA}$	50	$^{\circ}C/W$

Electrical Characteristics ($T_c=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.1	1.7	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	21.5	25	m Ω
		$V_{GS}=4.5V, I_D=20A$	-	30	38	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=20A$	-	19	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ $F=1.0MHz$	-	1256	-	pF
Output Capacitance	C_{oss}		-	285	-	pF
Reverse Transfer Capacitance	C_{rss}		-	39	-	pF
Switching Characteristics (Note 2)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=50V, I_D=20A$ $V_{GS}=10V, R_G=3\Omega$	-	8	-	nS
Turn-on Rise Time	t_r		-	50	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	29	-	nS
Turn-Off Fall Time	t_f		-	7	-	nS
Total Gate Charge	Q_g	$V_{DS}=50V, I_D=20A,$ $V_{GS}=10V$	-	26.3	-	nC
Gate-Source Charge	Q_{gs}		-	5.2	-	nC
Gate-Drain Charge	Q_{gd}		-	6.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=20A$	-	-	1.2	V
Diode Forward Current	I_S		-	-	31	A
Reverse Recovery Time	t_{rr}	$T_J = 25^{\circ}C, I_F = 20A$ $di/dt = 100A/\mu s$	-	40	-	nS
Reverse Recovery Charge	Q_{rr}		-	85	-	nC

Notes:

- EAS condition : $T_J=25^{\circ}C, V_{DD}=20V, V_G=10V, L=0.5mH, R_G=25\Omega$
- Guaranteed by design, not subject to production
- These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_J(MAX)=175^{\circ}C$. The SOA curve provides a single pulse rating.
- The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}C$. The maximum allowed junction temperature of $175^{\circ}C$. The value in any given application depends on the user's specific board design.

Typical Electrical and Thermal Characteristics

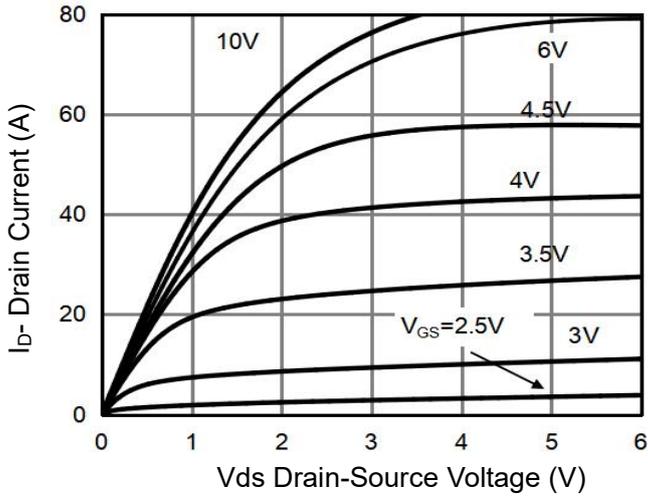


Figure 1 Output Characteristics

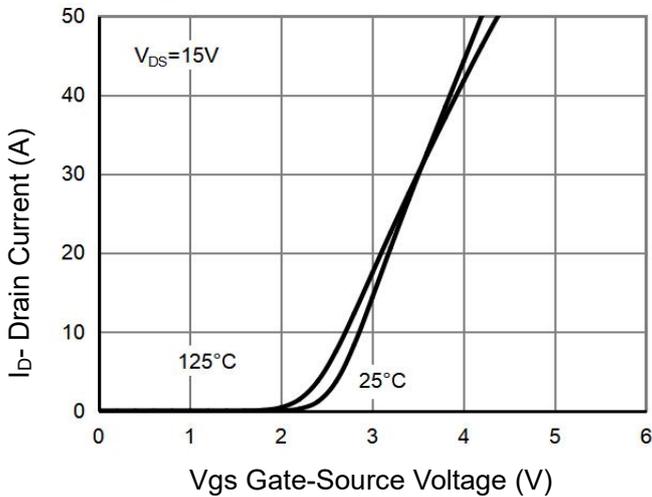


Figure 2 Transfer Characteristics

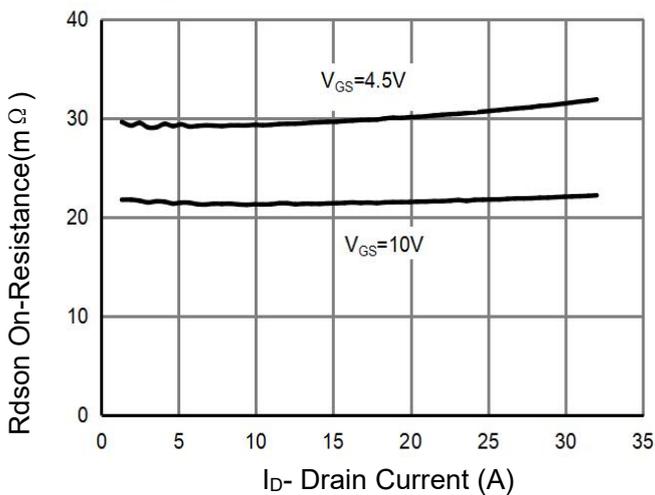


Figure 3 Rdson- Drain Current

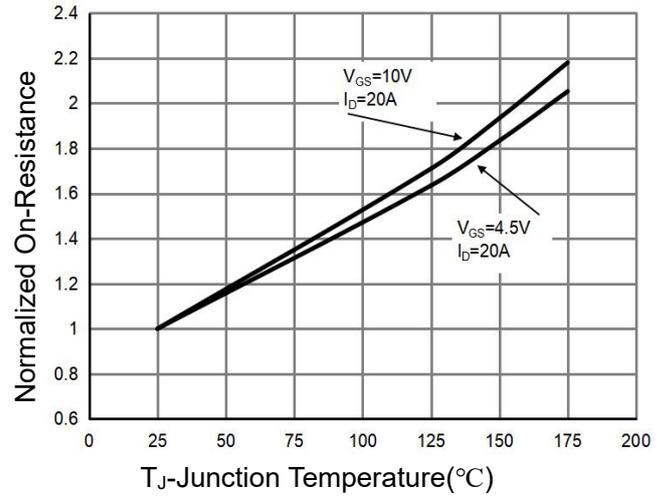


Figure 4 Rdson-Junction Temperature

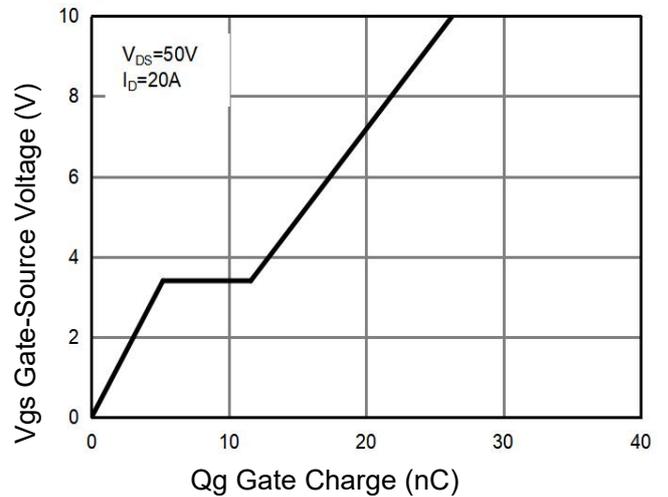


Figure 5 Gate Charge

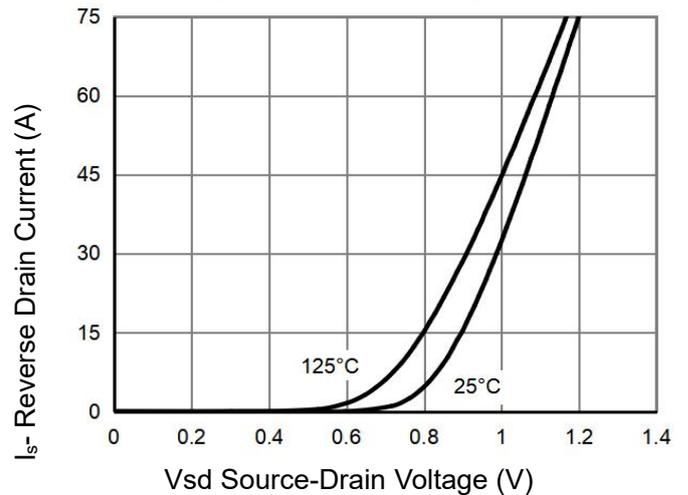


Figure 6 Source- Drain Diode Forward

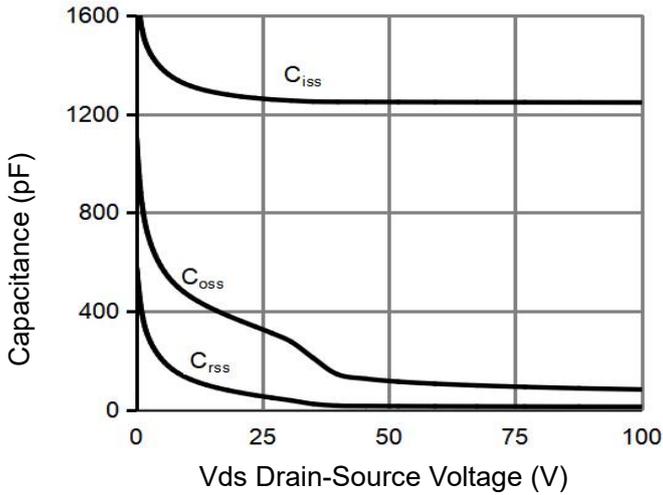


Figure 7 Capacitance vs Vds

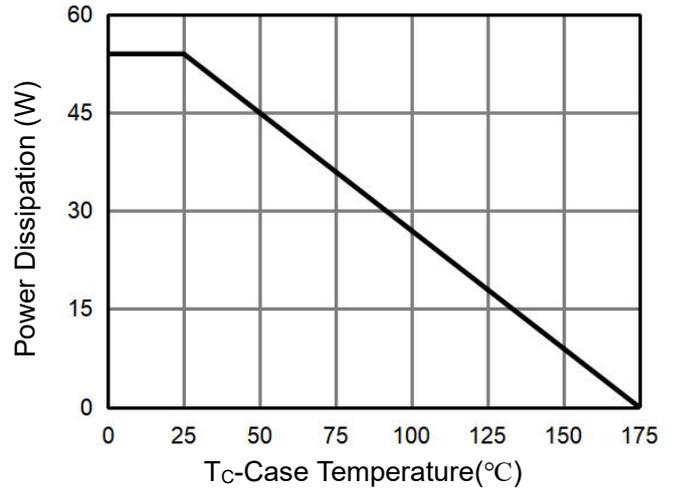


Figure 9 Power De-rating

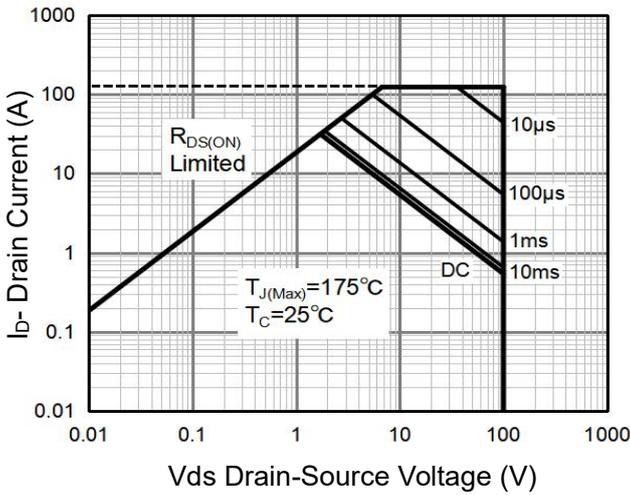


Figure 8 Safe Operation Area (Note3)

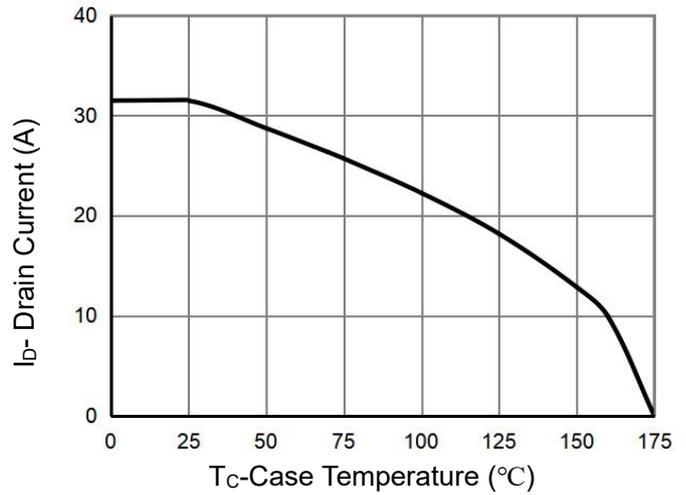


Figure 10 Current De-rating

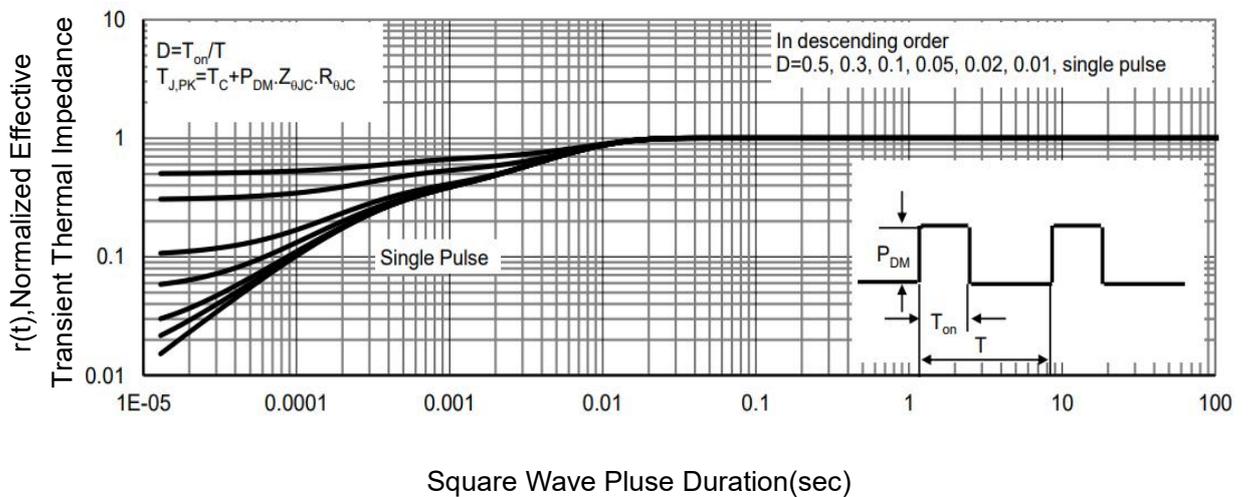
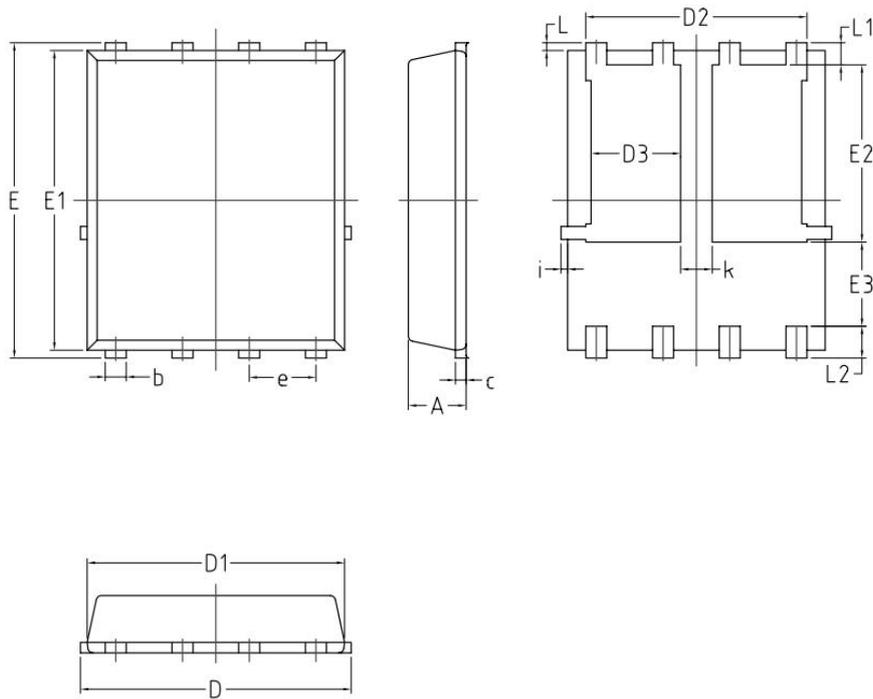


Figure 11 Normalized Maximum Transient Thermal Impedance

PDFN5X6-8L Package Information



SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.203 BSC		0.0080 BSC	
D	4.80	5.40	0.1890	0.2126
D1	4.80	5.00	0.1890	0.1969
D2	4.11	4.31	0.1620	0.1700
D3	1.60	1.80	0.0629	0.0708
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	3.30	3.50	0.1300	0.1378
E3	1.40	/	0.0551	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0019	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.71	0.0150	0.0280
i	/	0.18	/	0.0070
k	0.5	0.7	0.0197	0.0276

Revision History

Revision	Date	Subjects
V1.0	2024.10.22	Product data sheet

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